

Abstracts

A new 'T' circuit topology for the broadband modelling of symmetric inductors fabricated in CMOS technology (2001 [RFIC])

W. Tatinian, P. Pannier and R. Gillon. "A new 'T' circuit topology for the broadband modelling of symmetric inductors fabricated in CMOS technology (2001 [RFIC])." 2001 Radio Frequency Integrated Circuits (RFIC) Symposium 01. (2001 [RFIC]): 279-282.

This paper shows that the conventional /spl Pi/-type model is not suitable for the broadband modelling of symmetrical inductors. A T-shaped model topology is introduced and a suitable extraction procedure is proposed for its parameters. The new model allows one to reproduce the measured behaviour of symmetric inductors up to twice the self-resonance frequency.

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